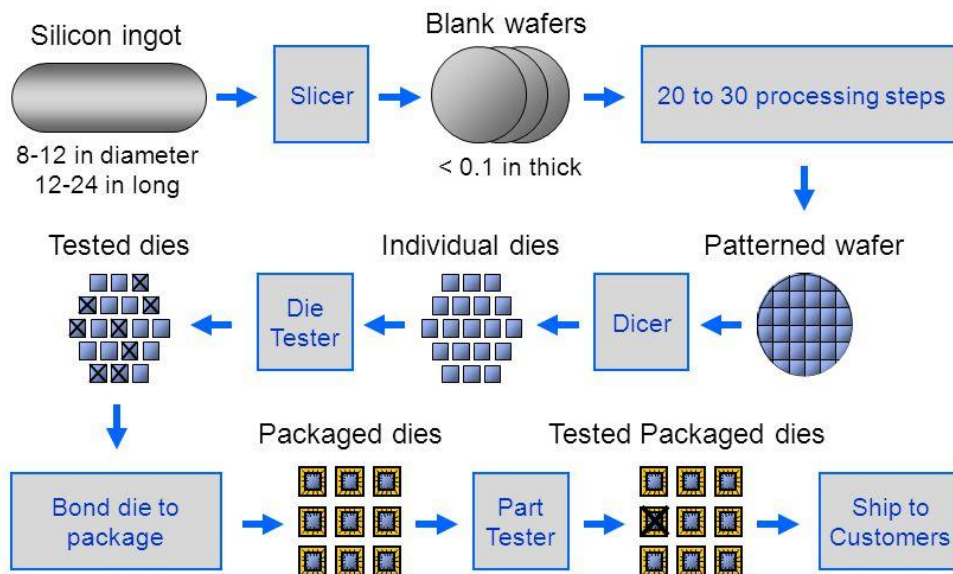


Manufacturing Process to create a “Chip”

J T Wunderlich PhD

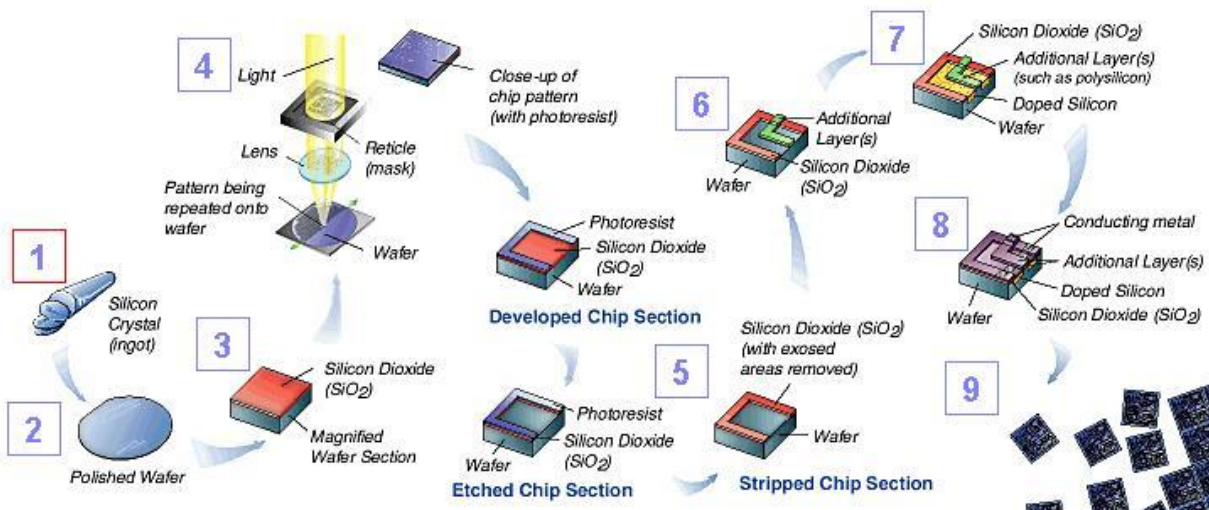
i.e., a chip of silicon with circuits on it, and packaged into what we usually refer to as the chip even though it is technically the chip package

Chip Manufacturing Process



<http://slideplayer.com/slide/5218297/16/images/1/Chip+Manufacturing+Process.jpg>

Some details of the process of creating circuits by creating layers:



<https://www.semiwiki.com/forum/attachments/f119/541d1300763089-semiconductor-fab-wiki-semiconductor-manufacturing-process.jpg>